

HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P. O. Box 272400  
Fort Collins, Colorado 80527-2400

## PATENT APPLICATION

ATTORNEY DOCKET NO. 10008019-1

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Venkitakrishnan et al.

Confirmation No.: 7710

Application No.: 09/942,328

Examiner: Kim, Hong Chong

Filing Date: 8/28/2001

Group Art Unit: 2186

Title: A STREAMLINED CACHE COHERENCY PROTOCOL SYSTEM AND METHOD FOR A  
MULTIPLE PROCESSOR SINGLE CHIP DEVICE

Mail Stop Amendment  
Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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FEB 14 2005

## TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment ( ) Petition to extend time to respond  
( ) New fee as calculated below ( ) Supplemental Declaration  
(X) No additional fee  
( ) Other: \_\_\_\_\_ (fee \$ \_\_\_\_\_)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	16	MINUS	21	= 0	X \$18	\$ 0
INDEP. CLAIMS	2	MINUS	3	= 0	X \$86	\$ 0
[ ] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$290	\$ 0
EXTENSION FEE	1ST MONTH \$110.00	2ND MONTH \$420.00	3RD MONTH \$930.00	4TH MONTH \$1480.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

(X) hereby certify that this paper is being transmitted  
to the Patent and Trademark Office facsimile  
number 17031-872-8306 on Feb. 14, 2005  
Number of pages: 10

Typed Name: Wendell J. Jones

Signature: 

Respectfully submitted,

Venkitakrishnan et al.

By 

Wendell J. Jones

Attorney/Agent for Applicant(s)  
Reg. No. 45,961

Date: Feb. 14, 2005

Rev 05/04 (TransFax)

Telephone No.: (408) 938-0980  
- Attach as First Page to Transmitted Papers -

Serial No.09/942,328  
HP Docket No: 10008019-1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of:

Venkitakrishnan et al.

Serial No.:09/942,328

Group Art Unit: 2186

Filed: August 28, 2001

Examiner: Kim, Hong Chong

For: A STREAMLINED CACHE COHERENCY PROTOCOL SYSTEM  
AND METHOD FOR A MULTIPLE PROCESSOR SINGLE CHIP  
DEVICE

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Honorable Commissioner of Patents and Trademarks  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Sir:

In response to the Office Action dated January 10, 2005, please enter the  
following amendments and remarks into the above-identified application:

Amendments begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.